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| Class | Subclass |
| ISSUE CLASSIFICATION | |

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PATENT NUMBER

U.S. UTILITY Patent Application

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| | O.I.P.E. | PATENT DATE |
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| APPLICATION NO. 09/955288 | CONT/PRIOR D F | CLASS 438 | SUBCLASS 371 | ART UNIT 2815 | EXAMINER L. J. J. |
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| TITLE | APPLICANTS |
|-------------------------|--------------------------------|
| 1. Chairman | 1. Mr. J. Edgar Hoover |
| 2. Vice Chairman | 2. Mr. J. Edgar Hoover |
| 3. Secretary | 3. Mr. J. Edgar Hoover |
| 4. Member | 4. Mr. J. Edgar Hoover |
| 5. Member | 5. Mr. J. Edgar Hoover |
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| 93. Member | 93. Mr. J. Edgar Hoover |
| 94. Member | 94. Mr. J. Edgar Hoover |
| 95. Member | 95. Mr. J. Edgar Hoover |
| 96. Member | 96. Mr. J. Edgar Hoover |
| 97. Member | 97. Mr. J. Edgar Hoover |

Joo-Hyong Lee

Latch-up resistant CMOS structure

PTO-2040
12/99

ISSUING CLASSIFICATION

| ORIGINAL | | | | CROSS REFERENCE(S) | | | | | |
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| CLASS | | SUBCLASS | | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | | | |
| INTERNATIONAL CLASSIFICATION | | | | | | | | | |
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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.. | _____ (Assistant Examiner) | | NOTICE OF ALLOWANCE MAILED | |
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| | (Date) | | Amount Due | Date Paid |
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